# **SIEMENS**



Manual

# **SIMATIC**

**S7-1500** 

CPU 1516-3 PN/DP (6ES7516-3AN02-0AB0)

**Edition** 

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# **SIEMENS**

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Preface

#### Legal information

#### Warning notice system

This manual contains notices you have to observe in order to ensure your personal safety, as well as to prevent damage to property. The notices referring to your personal safety are highlighted in the manual by a safety alert symbol, notices referring only to property damage have no safety alert symbol. These notices shown below are graded according to the degree of danger.

#### DANGER

indicates that death or severe personal injury will result if proper precautions are not taken.

# **A**WARNING

indicates that death or severe personal injury may result if proper precautions are not taken.

# **A**CAUTION

indicates that minor personal injury can result if proper precautions are not taken.

#### NOTICE

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# **Preface**

#### Purpose of the documentation

This manual supplements the system manual of the S7-1500 automation system/ET 200MP distributed I/O system as well as the function manuals. This manual contains a description of the module-specific information. The system-related functions are described in the system manual. All system-spanning functions are described in the function manuals.

The information provided in this manual and the system manual enables you to commission the CPU 1516-3 PN/DP.

#### Conventions

STEP 7: In this documentation, "STEP 7" is used as a synonym for all versions of the configuration and programming software "STEP 7 (TIA Portal)".

Please also observe notes marked as follows:

#### Note

A note contains important information on the product described in the documentation, on the handling of the product or on the section of the documentation to which particular attention should be paid.

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You can find catalogs for all automation and drive products on the Internet.

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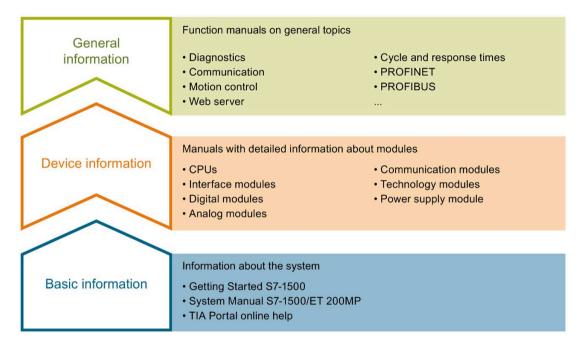
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S7-1500 / ET 200MP Documentation Guide

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The documentation for the SIMATIC S7-1500 automation system and the SIMATIC ET 200MP distributed I/O system is arranged into three areas.

This arrangement enables you to access the specific content you require.



#### **Basic information**

The System Manual and Getting Started describe in detail the configuration, installation, wiring and commissioning of the SIMATIC S7-1500 and ET 200MP systems. The STEP 7 online help supports you in the configuration and programming.

#### **Device information**

Product manuals contain a compact description of the module-specific information, such as properties, wiring diagrams, characteristics and technical specifications.

#### **General information**

The function manuals contain detailed descriptions on general topics regarding the SIMATIC S7-1500 and ET 200MP systems, e.g. diagnostics, communication, motion control, Web server, OPC UA.

You can download the documentation free of charge from the Internet (https://support.industry.siemens.com/cs/ww/en/view/109742691).

Changes and supplements to the manuals are documented in a Product Information.

You can download the product information free of charge from the Internet (https://support.industry.siemens.com/cs/us/en/view/68052815).

#### Manual Collection S7-1500/FT 200MP

The Manual Collection contains the complete documentation on the SIMATIC S7-1500 automation system and the ET 200MP distributed I/O system gathered together in one file.

You can find the Manual Collection on the Internet (https://support.industry.siemens.com/cs/ww/en/view/86140384).

#### SIMATIC S7-1500 comparison list for programming languages

The comparison list contains an overview of which instructions and functions you can use for which controller families.

You can find the comparison list on the Internet (https://support.industry.siemens.com/cs/ww/en/view/86630375).

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#### **Application examples**

The application examples support you with various tools and examples for solving your automation tasks. Solutions are shown in interplay with multiple components in the system - separated from the focus on individual products.

You will find the application examples on the Internet (https://support.industry.siemens.com/cs/ww/en/ps/ae).

Product overview 2

# 2.1 New functions

### New functions of the CPU in firmware version V2.9

The following table provides an overview of the most important new functions of the CPU with firmware version V2.9.

| New functions  | Applications  | Customer benefits  | Where can I find information?   |
|--|---|--|---|
| Communication of the CP  |   |  |   |
| Secure PG/HMI communication                                      | With STEP 7 and WinCC as of Version V17, SIMATIC S7-1500 CPUs and ET 200 CPUs from firmware version 2.9 support innovated and standardized secure PG/PC and HMI communication – referred to as Secure PG/HMI communication for short.   | Secure and standardized creation or assignment of PLC communication certificates   | Communication function manual (https://support.industry.siemens.com/cs/ww/en/view/59192925) |
| Protection of confidential configuration data                    | You have the option of assigning a password for protecting confidential configuration data of the respective CPU. This refers to data such as private keys that are required for the proper functioning of certificate-based protocols. | Additional password protection of confidential configuration data  |   |
| OPC UA: Alarms & Conditions                                      | OPC UA clients from any manufacturer can subscribe to alarms of the CPU via OPC UA Alarms & Conditions.   | Without further measures, alarms created once via OPC UA are available as events and alarms. The alarms can be displayed by any display device with corresponding OPC UA client functionality. |   |
| OPC UA: Certificate management via Global Discovery Server (GDS) | Via GDS push management functions:  Automated update of OPC UA certificates of an S7-1500 CPU  Transfer of updated certificates and lists in RUN operating state of the CPU   | The automation of the certificate management eliminates any manual work required for reconfiguring the CPU, for example, after a certificate has expired, and a new download to the CPU.       |   |

### 2.1 New functions

| New functions  | Applications   | Customer benefits   | Where can I find information?   |  |
|--|--|---|---|--|
| DHCP (Dynamic Host<br>Configuration Protocol)  | The CPU can use the DHCP communication protocol to assign the network configuration via a DHCP server.  The CPU uses a client ID for identifi-                       | With DHCP, you can integrate<br>the CPU into an existing IT<br>network without having to<br>make additional changes to                                    | Communication function manual (https://support.industry.siemens.com/cs/ww/                              |  |
|  | cation on the DHCP server. The following parameters can be   | the PROFINET interface.   | en/view/59192925)   |  |
|  | obtained:  • IP Suite  |   |   |  |
|  | DNS server   |   |   |  |
|  | NTP server   |   |   |  |
|  | The CPU can also send its host name to the DHCP server.  |   |   |  |
| MRP interconnection in<br>PROFINET networks  | The MRP interconnection procedure is an extension of MRP.  | There is no limit to the maximum number of 50 devices in  | PROFINET function manual  |  |
|  | MRP interconnection enables the redundant coupling of two or more rings with MRP in PROFINET networks.   | a ring when setting up redun-<br>dant network topologies.<br>Monitoring of larger topolo-<br>gies with ring redundancy                                    | (https://support.industry.siemens.com/cs/ww/en/view/49948856)   |  |
| Web server of the CPU  |  | <u> </u>  | I   |  |
| Web applications that can be loaded by the user  | Additional methods to manage web applications via Web API  | Web applications are also available in STOP mode of the   | Web server function manual  |  |
|  | You can use all available Web API methods within the web application   | CPU Reduced development times of web server user pages  | (https://support.industr<br>y.siemens.com/cs/ww/<br>en/view/59193560)                                   |  |
| New Web API methods  | Reading and changing the CPU operating state via Web API   | Additional applications for the web server  |   |  |
|  | Ticket methods for transferring large<br>amounts of data outside of the JSON<br>RPC protocol   |   |   |  |
| Diagnostic information on motion control   | Diagnostic information is available for all technology objects   | Comprehensive diagnostic options by means of motion   |   |  |
|  | Improved display and grouping of the tags  | control applications are possible via the web server.   |   |  |
| Optimizations to the DNS<br>(Domain Name System)<br>functionality with OPC<br>UA/Open User Communi-<br>cation and on the web | The feedback messages of the OPC UA server with the "Application Name" can be sent via DNS.  The NTP client of the CPU can address its relevant NTP servers via DNS. | A pool of NTP servers can be addressed.   | Communication function manual (https://support.industry.siemens.com/cs/ww/en/view/59192925)             |  |
| server   | The Web server can be consistently reached via DNS addressing. DNS is taken into account during certificate handling.  |   | Web server function manual (https://support.industry.siemens.com/cs/ww/en/view/59193560)                |  |
| Technology functions of t  | he CPU   |   |   |  |
| Axis functions   | <ul><li>Backlash compensation</li><li>Linear motor</li></ul>   | The repeat accuracy of a movement is increased by compensation for the mechanical play of the real axis.  Easier control of highly dynamic linear motors. | S7-1500T Motion Control function manuals (https://support.industry.siemens.com/cs/ww/en/view/109751049) |  |

#### Reference

You can find an overview of all new functions, improvements and revisions in the respective firmware versions on the Internet

(https://support.industry.siemens.com/cs/ww/en/view/109478459).

# 2.2 Area of application of the SIMATIC S7-1500 CPUs

#### Area of application

SIMATIC S7-1500 is the modular control system for a wide variety of automation applications in discrete automation.

SIMATIC S7-1500 is the cost-effective and convenient solution for a broad range of tasks and offers the following advantages:

- · Modular, fanless design
- Simple realization of distributed structures
- User-friendly handling

Areas of application of the SIMATIC S7-1500 automation system include, for example:

- Special-purpose machines
- · Textile machinery
- Packaging machines
- · General mechanical engineering
- · Controller engineering
- · Machine tool engineering
- · Installation engineering
- · Electrical industry and crafts
- · Automobile engineering
- Water/waste water
- Food & Beverage

Areas of application of the SIMATIC S7-1500R/H redundant system include, for example:

- Tunnels
- Airports (e.g. baggage conveyors)
- Subways
- Shipbuilding
- Wastewater treatment plants
- High-bay warehouses

Areas of application of the SIMATIC S7-1500T automation system for advanced motion control applications include, for example:

- · Packaging machines
- Converting applications
- · Assembly automation
- Pick-and-place automation
- Palletizers

You can choose between CPUs with various levels of performance and a comprehensive range of modules with many convenient functions. Fail-safe CPUs enable use in fail-safe applications. The modular design allows you to use only the modules that you need for your application. The controller can be retrofitted with additional modules at any time to expand its range of tasks.

High industrial suitability due to the high resistance to EMC, shock and vibration enable universal use of the SIMATIC S7-1500, S7-1500R/H and S7-1500T automation systems.

#### Performance segments of the CPUs

The CPUs can be used for smaller and mid-range applications, as well as for the high-end range of machine and plant automation.

Table 2- 1 Standard CPUs

| СРИ                  | Performance segment  | PROFIBUS<br>interfaces | PROFINET<br>IO RT/IRT<br>interfaces | PROFINET<br>IO RT<br>interface | Basic<br>PROFINET<br>functional-<br>ity | Work<br>memory | Pro-<br>cessing<br>time for<br>bit oper-<br>ations |
|----------------------|--|------------------------|-------------------------------------|--------------------------------|---|----------------|--|
| CPU 1511-1 PN        | Standard CPU for small<br>to mid-range applica-<br>tions                   |                        | 1                                   |                                |   | 1.15 MB        | 60 ns  |
| CPU 1513-1 PN        | Standard CPU for mid-<br>range applications                                |                        | 1                                   |                                |   | 1.8 MB         | 40 ns  |
| CPU 1515-2 PN        | Standard CPU for mid-<br>range to large applica-<br>tions                  |                        | 1                                   | 1                              |   | 3.5 MB         | 30 ns  |
| CPU 1516-<br>3 PN/DP | Standard CPU for de-<br>manding applications<br>and communication<br>tasks | 1                      | 1                                   | 1                              |   | 6 MB           | 10 ns  |
| CPU 1517-<br>3 PN/DP | Standard CPU for de-<br>manding applications<br>and communication<br>tasks | 1                      | 1                                   | 1                              |   | 10 MB          | 2 ns   |

| СРИ                      | Performance segment   | PROFIBUS interfaces | PROFINET<br>IO RT/IRT<br>interfaces | PROFINET<br>IO RT<br>interface | Basic<br>PROFINET<br>functional-<br>ity | Work<br>memory | Pro-<br>cessing<br>time for<br>bit oper-<br>ations |
|--------------------------|---|---------------------|-------------------------------------|--------------------------------|---|----------------|--|
| CPU 1518-4<br>PN/DP      | Standard CPU for high-<br>performance applica-<br>tions, demanding com-<br>munication tasks and<br>very short reaction<br>times                                       | 1                   | 1                                   | 1                              | 1                                       | 24 MB          | 1 ns   |
| CPU 1518-<br>4 PN/DP MFP | Standard CPU for high-<br>performance applica-<br>tions, demanding com-<br>munication tasks, very<br>short reaction times and<br>C/C++ blocks for the<br>user program | 1                   | 1                                   | 1                              | 1                                       | 74* MB         | 1 ns   |

<sup>\* 50</sup> MB of the integrated work memory is reserved for the function library of CPU runtime

Table 2- 2 Redundant CPUs

| СРИ                 | Performance segment   | PROFIBUS interfaces | PROFINET<br>IO RT/IRT<br>interfaces | PROFINET<br>IO RT<br>interface | Basic<br>PROFINET<br>functional-<br>ity | Work<br>memory | Pro-<br>cessing<br>time for<br>bit oper-<br>ations |
|---------------------|---|---------------------|-------------------------------------|--------------------------------|---|----------------|--|
| CPU 1513R-1 PN      | Redundant CPU for smaller to mid-range applications                                       |                     | 1                                   |                                |   | 1.8 MB         | 80 ns  |
| CPU 1515R-2 PN      | Redundant CPU for mid-<br>range to large applica-<br>tions                                |                     | 1                                   |                                | 1                                       | 3.5 MB         | 60 ns  |
| CPU 1517H-<br>3 PN  | Redundant CPU for<br>demanding applications<br>and communication<br>tasks                 |                     | 1                                   |                                | 1                                       | 10 MB          | 4 ns   |
| CPU 1518HF-<br>4 PN | Fail-safe and redundant<br>CPU for demanding<br>applications and com-<br>munication tasks |                     | 1                                   |                                | 2                                       | 69 MB          | 4 ns   |

Table 2- 3 Compact CPUs

| СРИ            | Performance segment                                     | PROFIBUS interfaces | PROFINET<br>IO RT/IRT<br>interfaces | PROFINET<br>IO RT<br>interface | Basic<br>PROFINET<br>functional-<br>ity | Work<br>memory | Pro-<br>cessing<br>time for<br>bit oper-<br>ations |
|----------------|---|---------------------|-------------------------------------|--------------------------------|---|----------------|--|
| CPU 1511C-1 PN | Compact CPU for small<br>to mid-range applica-<br>tions |                     | 1                                   |                                |   | 1.175 MB       | 60 ns  |
| CPU 1512C-1 PN | Compact CPU for mid-<br>range applications              |                     | 1                                   |                                |   | 1.25 MB        | 48 ns  |

Table 2- 4 Fail-safe CPUs

| СРИ                    | Performance segment  | PROFIBUS interfaces | PROFINET<br>IO RT/IRT<br>interfaces | PROFINET<br>IO RT<br>interface | Basic<br>PROFINET<br>functional-<br>ity | Work<br>memory | Pro-<br>cessing<br>time for<br>bit oper-<br>ations |
|------------------------|--|---------------------|-------------------------------------|--------------------------------|---|----------------|--|
| CPU 1511F-1 PN         | Fail-safe CPU for small<br>to mid-range applica-<br>tions                              |                     | 1                                   |                                |   | 1.225 MB       | 60 ns  |
| CPU 1511TF-<br>1 PN    | Fail-safe technology<br>CPU for small to mid-<br>range applications                    |                     | 1                                   |                                |   | 1.225 MB       | 60 ns  |
| CPU 1513F-1 PN         | Fail-safe CPU for mid-<br>range applications   |                     | 1                                   |                                |   | 1.95 MB        | 40 ns  |
| CPU 1515F-2 PN         | Fail-safe CPU for mid-<br>range to large applica-<br>tions                             |                     | 1                                   | 1                              |   | 3.75 MB        | 30 ns  |
| CPU 1515TF-<br>2 PN    | Fail-safe technology<br>CPU for demanding<br>applications and com-<br>munication tasks |                     | 1                                   | 1                              |   | 3.75 MB        | 30 ns  |
| CPU 1516F-<br>3 PN/DP  | Fail-safe CPU for de-<br>manding applications<br>and communication<br>tasks            | 1                   | 1                                   | 1                              |   | 6.5 MB         | 10 ns  |
| CPU 1516TF-<br>3 PN/DP | Fail-safe technology<br>CPU for demanding<br>applications and com-<br>munication tasks | 1                   | 1                                   | 1                              |   | 6.5 MB         | 10 ns  |
| CPU 1517F-<br>3 PN/DP  | Fail-safe CPU for de-<br>manding applications<br>and communication<br>tasks            | 1                   | 1                                   | 1                              |   | 11 MB          | 2 ns   |
| CPU 1517TF-<br>3 PN/DP | Fail-safe technology<br>CPU for demanding<br>applications and com-<br>munication tasks | 1                   | 1                                   | 1                              |   | 11 MB          | 2 ns   |

| СРИ                       | Performance segment  | PROFIBUS<br>interfaces | PROFINET<br>IO RT/IRT<br>interfaces | PROFINET<br>IO RT<br>interface | Basic<br>PROFINET<br>functional-<br>ity | Work<br>memory | Pro-<br>cessing<br>time for<br>bit oper-<br>ations |
|---------------------------|--|------------------------|-------------------------------------|--------------------------------|---|----------------|--|
| CPU 1518F-<br>4 PN/DP     | Fail-safe CPU for high-<br>performance applica-<br>tions, demanding com-<br>munication tasks and<br>very short reaction<br>times   | 1                      | 1                                   | 1                              | 1                                       | 26 MB          | 1 ns   |
| CPU 1518F-<br>4 PN/DP MFP | Fail-safe CPU for high-<br>performance applica-<br>tions, demanding com-<br>munication tasks, very<br>short reaction times and<br>C/C++ blocks for the<br>user program   | 1                      | 1                                   | 1                              | 1                                       | 76* MB         | 1 ns   |
| CPU 1518TF-<br>4 PN/DP    | Technology CPU for<br>high-performance mo-<br>tion control applications<br>with large quantities,<br>demanding communi-<br>cation tasks and very<br>short reaction times | 1                      | 1                                   | 1                              | 1                                       | 69 MB          | 1 ns   |

<sup>\* 50</sup> MB of the integrated work memory is reserved for the function library of CPU runtime

Table 2- 5 Technology CPUs

| СРИ                   | Performance segment  | PROFIBUS interfaces | PROFINET<br>IO RT/IRT<br>interfaces | PROFINET<br>IO RT<br>interface | Basic<br>PROFINET<br>functional-<br>ity | Work<br>memory | Pro-<br>cessing<br>time for<br>bit oper-<br>ations |
|-----------------------|--|---------------------|-------------------------------------|--------------------------------|---|----------------|--|
| CPU 1511T-1 PN        | Technology CPU for small to mid-range applications               |                     | 1                                   |                                |   | 1.225 MB       | 60 ns  |
| CPU 1515T-2 PN        | Technology CPU for<br>mid-range to large ap-<br>plications       |                     | 1                                   | 1                              |   | 3.75 MB        | 30 ns  |
| CPU 1516T-<br>3 PN/DP | Technology CPU for high-end applications and communication tasks | 1                   | 1                                   | 1                              |   | 6.5 MB         | 10 ns  |
| CPU 1517T-<br>3 PN/DP | Technology CPU for complex applications and communication tasks  | 1                   | 1                                   | 1                              |   | 11 MB          | 2 ns   |

| СРИ  | Performance segment  | PROFIBUS interfaces | PROFINET<br>IO RT/IRT<br>interfaces | PROFINET<br>IO RT<br>interface | Basic<br>PROFINET<br>functional-<br>ity | Work<br>memory | Pro-<br>cessing<br>time for<br>bit oper-<br>ations |
|--|--|---------------------|-------------------------------------|--------------------------------|---|----------------|--|
| CPU 1518T-4<br>PN/DP                             | Technology CPU for high-performance motion control applications with large quantities, demanding communication tasks and very short reaction times | 1                   | 1                                   | 1                              | 1                                       | 69 MB          | 1 ns   |
| CPU 1511TF-<br>1 PN<br>CPU 1515TF-<br>2 PN       | These CPUs are described   | in the fail-saf     | fe CPUs                             |                                |   |                |  |
| CPU 1516TF-<br>3 PN/DP                           |  |                     |                                     |                                |   |                |  |
| CPU 1517TF-<br>3 PN/DP<br>CPU 1518TF-<br>4 PN/DP |  |                     |                                     |                                |   |                |  |

# **Performance segments of compact CPUs**

The compact CPUs can be used for smaller to mid-range applications and have an integrated analog and digital on-board I/O as well as integrated technology functions. The following table shows the specific properties of the Compact CPUs.

|                                     | CPU 1511C-1 PN         | CPU 1512C-1 PN         |
|-------------------------------------|------------------------|------------------------|
| Integrated analog inputs/outputs    | 5 inputs/2 outputs     | 5 inputs/2 outputs     |
| Integrated digital inputs/outputs   | 16 inputs/16 outputs   | 32 inputs/32 outputs   |
| High-speed counters                 | 6                      | 6                      |
| Frequency meters                    | 6 (max. 100 kHz)       | 6 (max. 100 kHz)       |
| Period duration measurement         | 6 channels             | 6 channels             |
| Pulse width modulation (PWM output) |                        |                        |
|                                     | Max. 4 (up to 100 kHz) | Max. 4 (up to 100 kHz) |
| Pulse Train Output (PTO output)     | Max. 4 (up to 100 kHz) | Max. 4 (up to 100 kHz) |
| Frequency output                    | Up to 100 kHz          | Up to 100 kHz          |

#### **Integrated Motion Control technology functions**

**All CPUs of the SIMATIC S7-1500 automation system** support motion control technology functions. STEP 7 provides PLCopen-standardized Motion Control instructions for configuring and connecting a drive to the CPU.

S7-1500 Motion Control supports the following technology objects:

- Speed-controlled axes
- · Positioning axes
- · Synchronous axes
- External encoders
- Cam
- · Cam track
- Measuring input

The technology CPUs of the SIMATIC S7-1500-automation system offer enhanced Motion Control functions:

- Advanced synchronization functions
  - Synchronization with specification of the synchronous position
  - Actual value coupling
  - Shifting of the master value at the following axis
  - Camming
- Up to 4 encoder or measuring systems as actual position for position control
- Cam
- Kinematics for control of:
  - Cartesian portals
  - Roller pickers
  - Jointed-arm robots
  - Delta pickers
  - SCARA robots

Due to the supported technology functions, the S7-1500T CPUs are suitable for controlling packaging machines, converting applications, assembly automation, etc.

## Additional integrated technology functions

For effective commissioning, diagnostics and fast optimization of drives and controls, the SIMATIC S7-1500 controller series offers extensive trace functions for all CPU tags.

In addition to drive integration, the SIMATIC S7-1500 controller series has a PID compact closed-loop controller; easy-to-configure blocks allow automatic optimization of the controller parameters for optimum control quality.

#### Other technology functions

Technology modules also implement functions such as high-speed counting, position detection, measuring functions and pulse generators (PTO, PWM and frequency output). With the CPU 1511C-1 PN and CPU 1512C-1 PN compact CPUs, these functions are already integrated and can be implemented without additional technology modules.

SIWAREX is a versatile and flexible weighing module which you can use as a static scale for operation.

#### **Redundant CPUs**

The CPUs of the S7-1500R/H redundant system offer a high degree of reliability and system availability. A redundant configuration of the most important automation components reduces the likelihood of production downtimes and the consequences of component errors.

The higher the risks and costs of a production downtime, the more worthwhile the use of a redundant system. The avoidance of production downtimes compensates for the generally higher investment costs.

#### **Security Integrated**

In conjunction with STEP 7, each CPU offers password-based know-how protection against unauthorized reading out or modification of the program blocks.

Copy protection provides reliable protection against unauthorized reproduction of program blocks. With copy protection, individual blocks on the SIMATIC memory card can be tied to its serial number so that the block can only be run if the configured memory card is inserted into the CPU.

In addition, you can assign various access rights to different user groups in the controller using four different authorization levels.

Improved manipulation protection allows changed or unauthorized transfers of engineering data to be detected by the controller.

The use of an Ethernet CP (CP 1543-1) provides you with additional access protection through a firewall or possibilities to establish secure VPN connections.

#### Safety Integrated

The fail-safe CPUs are intended for users who want to implement demanding standard and fail-safe applications both centrally and decentrally.

These fail-safe CPUs allow the processing of standard and safety programs on a single CPU. This allows fail-safe data to be evaluated in the standard user program. The integration also provides the system advantages and the extensive functionality of SIMATIC for fail-safe applications.

The fail-safe CPUs are certified for use in safety mode up to:

- Safety class (Safety Integrity Level) SIL 3 according to IEC 61508:2010
- Performance Level (PL) e and Category 4 according to ISO 13849-1:2015 or EN ISO 13849-1:2015

Additional password protection for F-configuration and F-program is set up for IT security.

#### Design and handling

All CPUs of the SIMATIC S7-1500 product series feature a display with plain text information. The display provides the user with information on the order numbers, firmware version, and serial number of all connected modules. In addition, the IP address of the CPU and other network settings can be adapted locally without a programming device. Error messages are shown on the display directly in plain text. When performing servicing, you can minimize plant downtimes by quickly accessing the diagnostics alarms. Detailed information about this and a multitude of other display functions is available in the SIMATIC S7-1500 Display Simulator (https://support.industry.siemens.com/cs/ww/en/view/109761758).

Uniform front connectors for all modules and integrated potential jumpers for flexible formation of potential groups simplify storage. Additional components such as circuit breakers, relays, etc., can be installed quickly and easily, since a DIN rail is implemented in the rail of the SIMATIC S7-1500. The CPUs of the SIMATIC S7-1500 product series can be expanded centrally and in a modular fashion with signal modules. Space-saving expansion enables flexible adaptation to each application.

The system cabling for digital signal modules enables fast and clear connection to sensors and actuators from the field (fully modular connection consisting of front connector modules, connection cables and I/O modules), as well as easy wiring inside the control cabinet (flexible connection consisting of front connectors with assembled single conductors).

#### System diagnostics and alarms

Integrated system diagnostics is activated by default for the CPUs. The different types of diagnostics are configured instead of programmed. System diagnostics information and alarms from the drives are displayed consistently and in plain text:

- On the CPU display
- In STEP 7
- · On the HMI
- · On the Web server

This information is available in RUN mode, but also in STOP mode of the CPU. The diagnostic information is updated automatically when you configure new hardware components.

The CPU is available as a central interrupt server in up to three project languages. The HMI takes over the display in the project languages defined for the CPU. If you require alarm texts in additional languages, you can load them into your HMI via the configured connection. The CPU, STEP 7 and your HMI ensure data consistency without additional engineering steps. The maintenance work is easier.

# 2.3 Hardware properties

#### **Article number**

6ES7516-3AN02-0AB0

#### View of the module

The figure below shows the CPU 1516-3 PN/DP.



Figure 2-1 CPU 1516-3 PN/DP

#### Note

#### **Protective film**

Please note that the CPU is supplied with a removable protective film on the display.

# **Properties**

The 1516-3 PN/DP has the following technical properties:

| Property   | Description   | Additional information   |  |
|--|---|--|--|
| CPU display                                      | All CPUs of the SIMATIC S7-1500 product series feature a display with plain text information. The display provides information on order numbers, firmware version and serial numbers of all connected modules. In addition, you can set the IP address of the CPU and make further network settings. The display shows occurring error messages directly in plain text.  In addition to the functions listed here, a multitude of other functions that are described in the SIMATIC S7-1500 Display Simulator are shown on the display. | S7-1500, ET 200MP     (http://support.automation.sieme<br>ns.com/WW/view/en/59191792)<br>system manual     SIMATIC S7-1500 Display Simulator<br>(https://support.industry.siemens<br>.com/cs/ww/en/view/109761758) |  |
| Supply voltage                                   | The 24 V DC supply voltage is fed in via a 4-pin plug located at the bottom of the CPU.   | Section Connecting up (Page 32)     S7-1500, ET 200MP     (http://support.automation.siemens.com/WW/view/en/59191792)     system manual  |  |
| PROFINET IO                                      |   |  |  |
| PROFINET interface (X1<br>P1R and X1 P2R)        | The interface has two ports. In addition to basic PROFINET functionality, it also supports PROFINET IO RT (real time) and IRT (isochronous real time).  | PROFINET (https://support.industry.siemens.com/cs/ww/en/view/49948856) func-   |  |
| PROFINET interface<br>(X2 P1)                    | The interface has one port. In addition to basic PROFINET functionality, it also supports PROFINET IO RT (real time).   | tion manual  |  |
| Operation of the CPU as  IO controller  I-device | IO controller:     As an IO controller, the CPU addresses the connected IO devices     I-device:     As an I-device (intelligent IO device), the CPU is assigned to a higher-level IO controller and is used in the process as an intelligent pre-processing unit of sub-processes  |  |  |
| PROFIBUS DP                                      |   |  |  |
| PROFIBUS interface (X3)                          | The interface is used for connecting to a PROFIBUS network.   | PROFIBUS (https://support.industry.siemens.co  |  |
| Operation of the CPU as a DP master              | In the role as a DP master, the CPU addresses the connected DP slaves. It is not possible for the CPU to take the role of a DP slave.   | m/cs/ww/en/view/59193579) function manual  |  |

#### Accessories

You can find information on "Accessories/spare parts" in the S7-1500, ET 200MP system manual (http://support.automation.siemens.com/WW/view/en/59191792).

# 2.4 Firmware functions

### **Functions**

The CPU 1516-3 PN/DP supports the following firmware functions:

| Function                       | Description  | Additional information  |
|--------------------------------|--|---|
| Integrated system diagnostics  | The system automatically generates the alarms for the system diagnostics and outputs these alarms via a programming device/PC, HMI device, the Web server or the integrated display. System diagnostics information is also available when the CPU is in STOP mode.  | Diagnostics (https://support.industry.siemens.com/cs/ww/en/view/59192926) function manual   |
| Integrated Web server          | The Web server lets you access the CPU data by means of a network. Evaluations, diagnostics, and modifications are thus possible over long distances. Monitoring and evaluation is possible without STEP 7; all you need is a Web browser. Make sure that you take appropriate measures (e.g. limiting network access, using firewalls) to protect the CPU from being compromised.   | Web server     (https://support.industry.siemens .com/cs/ww/en/view/59193560) function manual     Security with SIMATIC S7 controllers     (https://support.industry.siemens .com/cs/ww/en/view/90885010) system manual |
| Integrated trace functionality | Trace functionality supports you in troubleshooting and/or optimizing the user program. You record device tags and evaluate the recordings with the trace and logic analyzer function. Tags are, for example, drive parameters or system and user tags of a CPU. The device saves the recordings. You can read out and permanently save the recordings with the configuration system (ES), if required. The trace and logic analyzer function is therefore suitable for monitoring highly dynamic processes. The trace recording can also be displayed through the Web server.                           | Using the trace and logic analyzer function (http://support.automation.siemens.com/WW/view/en/64897128) function manual   |
| OPC UA                         | With OPC UA, data is exchanged via an open and vendor-neutral communication protocol.  The CPU can act as OPC UA server. The CPU can communicate with OPC UA clients as an OPC UA server.  In turn, the CPU can access an OPC UA server as OPC UA client, allow the OPC UA server to run methods and read out information from the OPC UA server.  Through OPC UA Companion Specification, methods can be specified in a uniform and vendor-neutral way. The specified methods enable you to integrate devices from a wide range of manufacturers into your plants and production processes more easily. | Communication (https://support.industry.siemens.com/cs/ww/en/view/59192925) function manual   |
| Configuration control          | You can use configuration control to operate different real hardware configurations with a configured maximum configuration of the hardware. This means especially in series machine manufacturing you have the option of operating/configuring different configuration variants of a machine with a single project.   | S7-1500, ET 200MP<br>(http://support.automation.siemens.<br>com/WW/view/en/59191792) system<br>manual   |

| Function   | Description   | Additional information   |  |  |  |
|--|---|--|--|--|--|
| PROFINET IO                                      |   |  |  |  |  |
| RT (real time)                                   | RT prioritizes PROFINET IO frames over standard frames. This ensures the required determinism in the automation technology. In this process, the data is transferred via prioritized Ethernet frames.   | PROFINET<br>(https://support.industry.siemens.co<br>m/cs/ww/en/view/49948856) func-<br>tion manual |  |  |  |
| IRT (isochronous real time)                      | A reserved bandwidth within the send clock is available for IRT data. The reserved bandwidth ensures that the IRT data can be transmitted in time-synchronized intervals, unaffected by other high network loads (e.g. TCP/IP communication or additional real time communication). Update times with maximum determinism can be realized through IRT. Isochronous applications are possible with IRT.  |  |  |  |  |
| Isochronous mode                                 | The Isochronous mode system property records measured values and process data and processes the signals in a fixed system clock. Isochronous mode contributes to high control quality and hence to greater manufacturing precision. Isochronous mode reduces possible fluctuations of the process reaction times to a minimum. Time-assured processing makes higher machine cycles possible.  |  |  |  |  |
| MRP (Media Redundancy<br>Protocol)               | It is possible to establish redundant networks via the Media Redundancy Protocol. Redundant transmission links (ring topology) ensure that an alternative communication path is made available if a transmission link fails. The PROFINET devices that are part of this redundant network form an MRP domain.  RT operation is possible with the use of MRP.  |  |  |  |  |
| MDDD (Mardia Dadus dasas)                        |   | 1  |  |  |  |
| MRPD (Media Redundancy with Planned Duplication) | The advantage of the MRP extension MRPD is that, in the event of a failure of a device or a line in the ring, all other devices continue to be supplied with IO data without interruption and with short update times.  |  |  |  |  |
|  | MRPD is based on IRT and MRP. To realize media redundancy with short update times, the PROFINET devices participating in the ring send their data in both directions. The devices receive this data at both ring ports so that there is no ring reconfiguration time.   |  |  |  |  |
| Shared device                                    | The "Shared device" function allows you to divide the modules or submodules of an IO device up among different IO controllers. Numerous IO controllers are often used in larger or widely distributed systems. Without the "Shared device" function, each I/O module of an IO device is assigned to the same IO controller. If sensors that are physically close to each other must provide data to different IO controllers, several IO devices are required. The "Shared device" function allows the modules or submodules of an IO device to be divided up among different IO controllers, thus allowing flexible automation concepts. You can, for example, combine I/O modules that are physically close to each other in one IO device. |  |  |  |  |
| PROFlenergy                                      | PROFlenergy is a PROFINET-based data interface for switching off consumers centrally and with full coordination during pause times regardless of the manufacturer or device type. The goal is that the process is only provided with the energy that is absolutely required. Most of the energy is saved by the process. The PROFINET device itself only contributes a few watts to the savings potential.  |  |  |  |  |

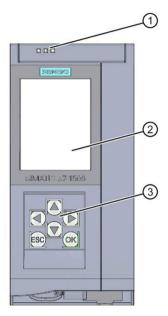
### 2.4 Firmware functions

| Function  | Description  | Additional information  |
|---|--|---|
| Integrated technology                           |  |   |
| Motion Control                                  | S7-1500 CPUs support the controlled positioning and traveling of axes via S7-1500 Motion Control functions by means of the following technology objects:  Speed-controlled axes, positioning axes, synchronized axes, external encoders, cams, cam tracks and measuring inputs | S7-1500T Motion Control function manuals (https://support.industry.siemens.com/cs/ww/en/view/109751049) |
|   | Speed-controlled axis for controlling a drive with speed specification   |   |
|   | Positioning axis for positioning of a drive with closed-loop position control  |   |
|   | Synchronous axis to interconnect with a master<br>value. The axis is synchronized to the master axis<br>position   |   |
|   | External encoder for detecting the actual position of<br>an encoder and its use as a master value for syn-<br>chronous operation   |   |
|   | Cams, cam track for position-dependent generation<br>of switching signals  |   |
|   | Measuring input for fast, accurate and event-<br>dependent sensing of actual positions   |   |
| Integrated closed-loop<br>control functionality | <ul> <li>PID Compact (continuous PID controller)</li> <li>PID 3Step (step controller for integrating actuators)</li> <li>PID Temp (temperature controller for heating and cooling with two separate actuators)</li> </ul>  | PID control (https://support.industry.siemens.com/cs/ww/en/view/108210036) function manual              |

| Function             | Description   | Additional information                                |
|----------------------|---|---|
| Integrated safety    |   |   |
| Know-how protection  | The know-how protection protects user blocks against unauthorized access and modifications.   | S7-1500, ET 200MP (http://support.automation.siemens. |
| Copy protection      | Copy protection links user blocks with the serial number of one or more SIMATIC memory cards or the serial number of one or more CPUs. User programs cannot run without the corresponding SIMATIC memory card or CPU. | com/WW/view/en/59191792) system manual                |
| Access protection    | You can use authorization levels to assign separate rights to different users.  |   |
| Integrity protection | The CPUs feature integrity protection by default. Integrity protection identifies possible manipulation of engineering data on the SIMATIC memory card or during data transfer between STEP 7 and the CPU.            |   |
|                      | Integrity protection also checks the communication from a SIMATIC HMI system to the CPU for possible manipulation of engineering data.  |   |
|                      | If integrity protection identifies the manipulation of engineering data, the user receives a corresponding message.   |   |
| Password provider    | As an alternative to manual password input, you can connect a password provider to STEP 7. A password provider offers the following advantages:   |   |
|                      | <ul> <li>Convenient handling of passwords. STEP 7 reads in<br/>the password automatically for the blocks. This<br/>saves you time.</li> </ul>   |   |
|                      | Optimum block protection because the users them-<br>selves do not know the password.  |   |

# 2.5.1 Front view of the CPU with closed front panel

The following figure shows the front view of the CPU 1516-3 PN/DP.



- ① LEDs for the current operating mode and diagnostic status of the CPU
- ② Display
- ③ Operator control buttons

Figure 2-2 View of the CPU 1516-3 PN DP (with front panel) - front

#### Note

#### Temperature range for display

To increase its service life, the display switches off at a temperature below the permitted operating temperature of the device. When the display cools down, it automatically switches itself on again. When the display is switched off, the LEDs continue to show the status of the CPU.

For more information on the temperatures at which the display switches itself on and off, refer to the Technical specifications (Page 41).

#### Removing and fitting the front panel or display

You can remove and fit the front panel or the display during operation.



#### Personal injury and damage to property may occur

Personal injury or material damage can occur in zone 2 hazardous areas if you remove or fit the display while the S7-1500 automation system is running.

Before you remove or insert the display in zone 2 hazardous areas, always make sure that the power supply to the S7-1500 automation system is switched off.

#### Locking the front panel

You can lock the front panel to protect the SIMATIC memory card and the operating mode buttons of the CPU against unauthorized access.

You can attach a security seal or a padlock with a diameter of 3 mm to the front panel.

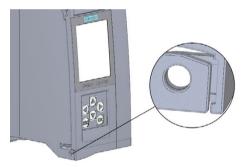


Figure 2-3 Locking latch on the CPU

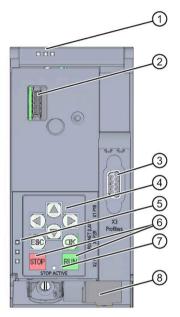
In addition to the mechanical lock, you can also block access to a password-protected CPU on the display (local lock) and assign a password for the display. You can find additional information on the display, configurable protection levels and local locks in the S7-1500/ET 200MP (http://support.automation.siemens.com/WW/view/en/59191792) system manual.

#### Reference

You will find detailed information on the individual display options, a training course and a simulation of the available menu commands in the SIMATIC S7-1500 Display Simulator (https://support.industry.siemens.com/cs/ww/en/view/109761758).

# 2.5.2 Front view of the CPU without front panel or display and view from below

The following figure shows the operator controls and connection elements of the CPU 1516-3 PN/DP.



- ① LEDs for the current operating mode and diagnostic status of the CPU
- 2 Connector for the display
- ③ PROFIBUS interface (X3)
- (4) Arrow keys
- 5 LED displays for the PROFINET interface
- 6 STOP and RUN operating mode buttons
- (7) STOP-ACTIVE LED
- 8 Connector for the supply voltage

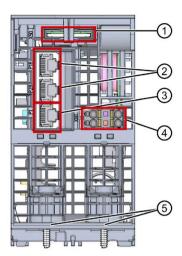
Figure 2-4 View of the CPU 1516-3 PN/DP (without front panel or display) - front

#### Note

#### Removing the display

Only remove the display if it is faulty.

You can find information on removing and replacing displays in the S7-1500, ET 200MP system manual (<a href="http://support.automation.siemens.com/WW/view/en/59191792">http://support.automation.siemens.com/WW/view/en/59191792</a>).

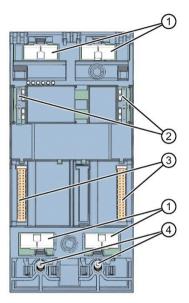


- ① Slot for the SIMATIC memory card
- ② PROFINET IO interface (X1) with 2 ports
- 3 PROFINET IO interface (X2) with 1 port
- 4 Connector for supply voltage
- ⑤ Fastening screw

Figure 2-5 View of the CPU 1516-3 PN/DP - bottom

# 2.5.3 Rear view of the CPU

The following figure shows the connection elements on the rear of the CPU 1516-3 PN/DP.



- Shield contact surfaces
- 2 Plug-in connection for power supply
- 3 Plug-in connection for backplane bus
- Fixing screws

Figure 2-6 View of the CPU 1516-3 PN/DP - rear

# 2.6 Operating mode buttons

You use the operating mode buttons to:

- Request a change to a specific operating state
- Disable or enable the change to a specific operating state

  (if the operating mode button STOP is active, for example, you cannot switch the CPU to
  RUN via a communication task configured in the TIA Portal or via the display)

The following table shows the meaning of the corresponding operation of the operating mode buttons.

Table 2- 6 Meaning of the operating mode buttons

| Operation of the operating mode buttons  | Meaning   | Explanation  |
|--|---|--|
| RUN  | RUN mode  | The CPU has permission to go to RUN.   |
| STOP   | STOP mode   | The CPU does not have permission to go to RUN.   |
| 1. Press the operating mode button STOP.  Result: The RUN/STOP LED lights up yellow.  2. Press the operating mode button STOP until the RUN/STOP LED lights up for the 2nd time and remains continuously lit (this takes three seconds). After this, release the button. | Manual memory reset (with SIMATIC memory card inserted) or Reset to factory settings (without inserted SIMATIC memory card) | The CPU is executing a memory reset.  or The CPU is being reset to factory settings. You can find additional information in the S7- 1500/ET 200MP system manual (https://support.industry.siemens.com/cs/ww/en/view/59191792). |
| Press the operating mode button STOP again within the next three seconds.  |   |  |

Connecting up

This section provides information on the terminal assignment of the individual interfaces and the block diagram of the CPU 1516-3 PN/DP.

# 24 V DC supply voltage (X80)

The connector for the power supply is plugged in when the CPU ships from the factory.

The following table shows the signal names and the descriptions of the pin assignment of the 24 V DC supply voltage.

Table 3-1 Pin assignment 24 V DC supply voltage

| View   | Si | gnal name 1) | Description  |
|--|----|--------------|--|
| Connector  |    |              |  |
| <u>a</u> a   | 1  | 1L+          | + 24 V DC of the supply voltage                                |
| (2) (3)<br>1Ml 2Ml   | 2  | 1M           | Ground of the supply voltage                                   |
| 2101   | 3  | 2M           | Ground of the supply voltage for loop-through 2)               |
| do la constantina de la constantina della consta | 4  | 2L+          | + 24 V DC of the supply voltage for loop-through <sup>2)</sup> |
| 1L+ 2L+  |    |              |  |
| ① ④  |    |              |  |

<sup>1) 1</sup>L+ and 2L+ as well as 1M and 2M are bridged internally

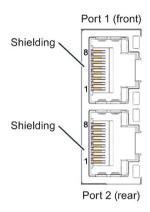
If the CPU is supplied by a system power supply, it is not necessary to connect the 24 V supply.

<sup>2)</sup> Maximum 10 A permitted

## PROFINET interface X1 with 2-port switch (X1 P1R and X1 P2R)

The assignment corresponds to the Ethernet standard for an RJ45 plug.

- When autonegotiation is deactivated, the RJ45 socket is allocated as a switch (MDI-X).
- When autonegotiation is activated, autocrossing is in effect and the RJ45 socket is allocated either as data terminal equipment (MDI) or a switch (MDI-X).



# PROFINET interface X2 with 1 port (X2 P1)

The assignment corresponds to the Ethernet standard for an RJ45 plug.

Autocrossing is always active on X2. This means the RJ45 socket is allocated either as data terminal equipment (MDI) or a switch (MDI-X).

# Removing the PROFINET plug

You need a screwdriver (max. blade width 2.5 mm) to remove the PROFINET plug.

#### **PROFIBUS** interface X3

The table below shows the terminal assignment of the PROFIBUS interface. The assignment corresponds to the standard assignment of an RS485 interface.

Table 3- 2 PROFIBUS interface terminal assignment

| View     | Signal name |           | Description                             |
|----------|-------------|-----------|---|
|          | 1           | -         | -                                       |
|          | 2           | -         | -                                       |
| 5        | 3           | RxD/TxD-P | Data line B                             |
| 9 8 4 3  | 4           | RTS       | Request To Send                         |
| 7 2 1    | 5           | M5V2      | Data reference potential (from station) |
|          | 6           | P5V2      | Supply plus (from station)              |
|          | 7           | -         | -                                       |
| X3       | 8           | RxD/TxD-N | Data line A                             |
| PROFIBUS | 9           | ı         | -                                       |

#### Note

#### Supply of I/O devices

The CPU 1516-3 PN/DP does not provide a 24 V DC power supply on the PROFIBUS interface. I/O devices (for example, PC adapter USB 6ES7972-0CB20-0XA0) are only operational on the interface in conjunction with a plug-in power supply set for external power supply.

The innovative successor product, PC adapter USB A2, receives the required power supply via the USB port. This means it does not need a 24 V DC supply voltage and can be operated **without** a plug-in power supply set for external power supply.

#### Removing the display

You can find a description of how to remove and replace the display in the S7-1500, ET 200MP (http://support.automation.siemens.com/WW/view/en/59191792) system manual.

#### Reference

You can find additional information on the topics of "Connecting the CPU" and "Accessories/spare parts" in the S7-1500, ET 200MP (http://support.automation.siemens.com/WW/view/en/59191792) system manual.

### Assignment of the MAC addresses

CPU 1516-3 PN/DP has two PROFINET interfaces, with the first interface having two ports. The PROFINET interfaces each have a MAC address, and each of the PROFINET ports has its own MAC address. The CPU 1516-3 PN/DP therefore has five MAC addresses in total.

The MAC addresses of the PROFINET ports are needed for the LLDP protocol, for example for the neighborhood discovery function.

The number range of the MAC addresses is sequential. The first and last MAC addresses are printed on the rating plate on the right side of each CPU 1516-3 PN/DP.

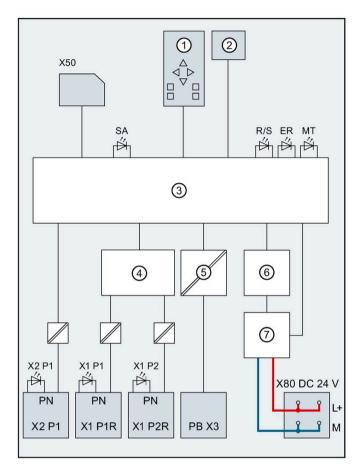
The table below shows how the MAC addresses are assigned.

Table 3- 3 Assignment of the MAC addresses

|               | Assignment  | Labeling   |
|---------------|---|--|
| MAC address 1 | PROFINET interface X1 (visible in STEP 7 in accessible devices) | <ul><li>Front printed</li><li>Right side printed<br/>(start of number range)</li></ul> |
| MAC address 2 | Port X1 P1R (required for LLDP, for example)                    |  |
| MAC address 3 | Port X1 P2R (required for LLDP, for example)                    |  |
| MAC address 4 | PROFINET interface X2 (visible in STEP 7 in accessible devices) | Front printed  |
| MAC address 5 | Port X2 P1 (required for LLDP, for example)                     | Right side printed (end of number range)   |

# **Block diagram**

The following figure shows the block diagram of the CPU 1516-3 PN/DP.



| 1           | CPU with control and operating mode but-     | PN X1 P2 R    | PROFINET interface X1 port 2 |
|-------------|--|---------------|------------------------------|
|             | tons   |               |                              |
| 2           | Display                                      | PN X2 P1      | PROFINET interface X2 port 1 |
| 3           | Electronics                                  | PB X3         | PROFIBUS interface X3        |
| 4           | PROFINET 2-port switch                       | L+            | 24 V DC supply voltage       |
| 5           | PROFIBUS DP driver with electrical isolation | M             | Ground                       |
| 6           | Backplane bus interface                      | SA            | STOP-ACTIVE LED (yellow)     |
| 7           | Internal supply voltage                      | R/S           | RUN/STOP LED (yellow/green)  |
| X50         | SIMATIC memory card                          | ER            | ERROR LED (red)              |
| X80 24 V DC | Infeed of supply voltage                     | MT            | MAINT LED (yellow)           |
| PN X1 P1 R  | PROFINET interface X1 port 1                 | X1 P1, X1 P2, | LED Link TX/RX               |
|             |  | X2 P1         |                              |

Figure 3-1 Block diagram of the CPU 1516-3 PN/DP

# Interrupts, error messages, diagnostics and system alarms

The LED displays of the CPU 1516-3 PN/DP are described below.

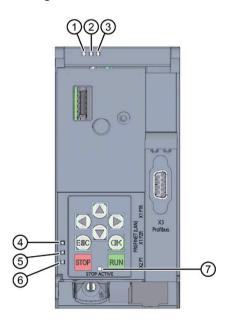
You will find additional information on "Interrupts" in the STEP 7 online help.

You can find additional information on the topics of "Diagnostics" and "System alarms" in the Diagnostics (<a href="http://support.automation.siemens.com/WW/view/en/59192926">http://support.automation.siemens.com/WW/view/en/59192926</a>) function manual.

# 4.1 Status and error display of the CPU

#### **LED** display

The figure below shows the CPU 1516-3 PN/DP LEDs.



- 1 RUN/STOP LED (green/yellow LED)
- ② ERROR LED (red LED)
- MAINT LED (yellow LED)
- 4 LINK RX/TX LED for port X1 P1 (yellow/green LED)
- (5) LINK RX/TX LED for port X1 P2 (yellow/green LED)
- 6 LINK RX/TX LED for port X2 P1 (yellow/green LED)
- STOP-ACTIVE LED

Figure 4-1 LED displays of the CPU 1516-3 PN/DP (without front panel and display)

#### 4.1 Status and error display of the CPU

#### Meaning of the RUN/STOP, ERROR and MAINT LEDs

The CPU 1516-3 PN/DP has three LEDs for displaying the current operating state and diagnostic status. The following table shows the meaning of the various combinations of colors for the RUN/STOP, ERROR and MAINT LEDs.

Table 4-1 Meaning of the LEDs

| RUN/STOP LED       | ERROR LED                             | MAINT LED           | Meaning   |
|--------------------|---------------------------------------|---------------------|---|
| 8                  |                                       |                     | Missing or insufficient power supply on the CPU.  |
| LED off            | LED off                               | LED off             |   |
| <b>=</b>           | 崇                                     |                     | An error has occurred.  |
| LED off            | LED flashes red                       | LED off             |   |
|                    |                                       |                     | CPU is in RUN mode.   |
| LED lit green      | LED off                               | LED off             | There are no events, requirements, errors, etc.   |
|                    | 崇                                     |                     | A diagnostics event is pending.   |
| LED lit green      | LED flashes red                       | LED off             |   |
|                    |                                       |                     | Maintenance demanded for the plant.   |
| LED lit green      | LED off                               | LED lit yellow      | You need to check/replace the affected hardware within a short period of time.                          |
|                    |                                       |                     | Active Force job  |
|                    |                                       | 崇                   | Bad configuration   |
| LED lit green      | LED off                               | LED flashes yellow  |   |
| -                  | 崇                                     |                     | A diagnostics event is pending.   |
| LED lit yellow     | LED flashes red                       | LED off             |   |
| -                  |                                       | 崇                   | Firmware update successfully completed.   |
| LED lit yellow     | LED off                               | LED flashes yellow  |   |
| _                  |                                       |                     | CPU is in STOP mode.  |
| LED lit yellow     | LED off                               | LED off             |   |
| LED lit yellow     | <del>汶</del><br>LED flashes red       | LED flashes yellow  | The program on the SIMATIC memory card is causing an error.   |
| EED me yenow       | LED Hasiles led                       | LLD flasfies yellow | Firmware update using SIMATIC memory card has failed.   |
|                    |                                       |                     | The CPU has detected an error state. Additional information is available via the CPU diagnostic buffer. |
| 茶                  |                                       |                     | CPU is performing internal activities during STOP, e.g. startup after STOP.                             |
| LED flashes yellow | LED off                               | LED off             | Download of the user program from the SIMATIC memory card   |
|                    |                                       |                     | CPU is executing a program with active breakpoint.  |
| 計<br>LED flashes   | LED off                               | LED off             | Startup (transition from STOP → RUN)  |
| yellow/green       |                                       |                     | Contract (CDUI and and  |
| 祟                  | · · · · · · · · · · · · · · · · · · · | ※                   | Startup (CPU booting)  Test of LEDs during startup, inserting a module                                  |
| LED flashes        | LED flashes red                       | LED flashes yellow  | Test of LEDs during startup, inserting a module.  LED flashing test                                     |
| yellow/green       |                                       |                     | בבט וומאווווש נפאנ  |

## Meaning of LINK RX/TX LED

Each port has a LINK RX/TX LED. The table below shows the various "LED scenarios" of the ports of the CPU 1516-3 PN/DP.

Table 4- 2 Meaning of the LED

| LINK TX/RX LED                        | Meaning   |
|---------------------------------------|---|
| ⊟<br>LED off                          | There is no Ethernet connection between the PROFINET interface of the PROFINET device and the communication partner.                |
| LLD OII                               | No data is currently being sent/received via the PROFINET interface.  |
|                                       | There is no LINK connection.  |
| · · · · · · · · · · · · · · · · · · · | The CPU is performing an "LED flash test".  |
| LED flashes green                     |   |
|                                       | There is an Ethernet connection between the PROFINET interface of your PROFINET device  |
| LED lit green                         | and a communication partner.  |
| LED flashes                           | Data is currently being received/sent by a communication partner in the Ethernet via the PROFINET interface of the PROFINET device. |

#### Note

#### "LED" instruction

You can read the status (e.g. "On" or "Off") of LEDs of a CPU or a module using the "LED" instruction. Note, however, that it is not possible to read the LED status of the LINK RX/TX LEDs on all S7-1500 CPUs.

You can find additional information on the "LED" instruction in the STEP 7 online help.

4.1 Status and error display of the CPU

# Meaning of STOP-ACTIVE LED

The following table shows the meaning of the STOP-ACTIVE LED for the CPU 1516-3 PN/DP.

Table 4-3 Meaning of the LED

| STOP-ACTIVE LED | Meaning  |
|-----------------|--|
|                 | The CPU has been switched to STOP mode using the STOP button.  |
| LED lit yellow  | As long as the STOP-ACTIVE LED is lit, switching the CPU to RUN mode is only possible using the RUN button.  |
|                 | The CPU can then no longer be set to RUN mode via display operation or via online functions. The state of the buttons is retained at power-off. If the CPU should not start up automatically after a power-on, you have to keep the STOP button pressed during startup until the STOP-ACTIVE LED is activated. |
|                 | If automatic startup is to be reliably prevented after a power-on, the STOP button has to be kept pressed during the startup of the CPU until the STOP-ACTIVE LED is activated.  |
| LED off         | The CPU has been set to STOP mode using the display or programming device/PC and not with the STOP button on the device.   |
|                 | The CPU is in RUN mode.  |

Technical specifications

The following table shows the technical specifications as of 05/2021. You can find a data sheet including daily updated technical specifications on the Internet (https://support.industry.siemens.com/cs/ww/en/pv/6ES7516-3AN02-0AB0/td?dl=en).

| Article number  | 6ES7516-3AN02-0AB0   |
|---|--|
| General information                                       |  |
| Product type designation                                  | CPU 1516-3 PN/DP   |
| HW functional status                                      | FS01   |
| Firmware version  | V2.9   |
| Product function  |  |
| I&M data  | Yes; I&M0 to I&M3  |
| Isochronous mode  | Yes; Distributed and central; with minimum OB 6x cycle of 375 µs (distributed) and 1 ms (central)          |
| Engineering with  |  |
| STEP 7 TIA Portal configurable/integrated<br>from version | V17 (FW V2.9) / V16 (FW V2.8) or higher; with older TIA Portal versions configurable as 6ES7516-3AN01-0AB0 |
| Configuration control                                     |  |
| via dataset   | Yes  |
| Display   |  |
| Screen diagonal [cm]                                      | 6.1 cm   |
| Control elements  |  |
| Number of keys  | 8  |
| Mode buttons  | 2  |
| Supply voltage  |  |
| Type of supply voltage                                    | 24 V DC  |
| permissible range, lower limit (DC)                       | 19.2 V   |
| permissible range, upper limit (DC)                       | 28.8 V   |
| Reverse polarity protection                               | Yes  |
| Mains buffering   |  |
| Mains/voltage failure stored energy time                  | 5 ms   |
| Repeat rate, min.   | 1/s  |
| Input current   |  |
| Current consumption (rated value)                         | 0.85 A   |
| Current consumption, max.                                 | 1.1 A  |
| Inrush current, max.                                      | 2.4 A; Rated value   |
| l <sup>2</sup> t  | 0.02 A <sup>2</sup> ⋅s   |
| Power   |  |
| Infeed power to the backplane bus                         | 12 W   |
| Power consumption from the backplane bus (balanced)       | 6.7 W  |
| Power loss  |  |
| Power loss, typ.  | 7 W  |

| Article number   | 6ES7516-3AN02-0AB0  |
|--|---|
| Memory   |   |
| Number of slots for SIMATIC memory card                            | 1   |
| SIMATIC memory card required                                       | Yes   |
| Work memory  |   |
| <ul> <li>integrated (for program)</li> </ul>                       | 1 Mbyte   |
| • integrated (for data)  | 5 Mbyte   |
| Load memory  |   |
| • Plug-in (SIMATIC Memory Card), max.                              | 32 Gbyte  |
| Backup   |   |
| maintenance-free   | Yes   |
| CPU processing times   |   |
| for bit operations, typ.   | 10 ns   |
| for word operations, typ.  | 12 ns   |
| for fixed point arithmetic, typ.                                   | 16 ns   |
| for floating point arithmetic, typ.                                | 64 ns   |
| CPU-blocks Number of elements (total)                              | 8 000; Blocks (OB, FB, FC, DB) and UDTs   |
| DB   | o dod, blocks (ob, 1 b, 1 c, bb) and ob13   |
| Number range   | 1 60 999; subdivided into: number range that  |
| Number range   | can be used by the user: 1 59 999, and number   |
|  | range of DBs created via SFC 86: 60 000 60 999 5 Mbyte; For DBs with absolute addressing, the |
| • Size, max.   | max. size is 64 KB  |
| FB   |   |
| Number range   | 0 65 535  |
| • Size, max.   | 1 Mbyte   |
| FC   |   |
| Number range   | 0 65 535  |
| Size, max.   | 1 Mbyte   |
| ОВ   |   |
| Size, max.   | 1 Mbyte   |
| Number of free cycle OBs   | 100   |
| Number of time alarm OBs   | 20  |
| Number of delay alarm OBs  | 20  |
| Number of cyclic interrupt OBs                                     | 20; With minimum OB 3x cycle of 250 μs  |
|  | 50  |
| Number of process alarm OBs  |   |
| Number of DPV1 alarm OBs   | 3   |
| Number of isochronous mode OBs                                     | 3   |
| <ul> <li>Number of technology synchronous alarm<br/>OBs</li> </ul> | 2   |
| Number of startup OBs  | 100   |
|  |   |

| Additional   | CECTEAC DANIES CARE   |
|--|---|
| Article number   | 6ES7516-3AN02-0AB0  |
| Number of asynchronous error OBs                                   | 4   |
| <ul> <li>Number of synchronous error OBs</li> </ul>                | 2   |
| <ul> <li>Number of diagnostic alarm OBs</li> </ul>                 | 1   |
| Nesting depth  |   |
| <ul> <li>per priority class</li> </ul>                             | 24  |
| Counters, timers and their retentivity                             |   |
| S7 counter   |   |
| <ul> <li>Number</li> </ul>   | 2 048   |
| Retentivity  |   |
| – adjustable   | Yes   |
| IEC counter  |   |
| <ul> <li>Number</li> </ul>   | Any (only limited by the main memory)   |
| Retentivity  |   |
| – adjustable   | Yes   |
| S7 times   |   |
| <ul> <li>Number</li> </ul>   | 2 048   |
| Retentivity  |   |
| <ul><li>adjustable</li></ul>                                       | Yes   |
| IEC timer  |   |
| <ul> <li>Number</li> </ul>   | Any (only limited by the main memory)   |
| Retentivity  |   |
| – adjustable   | Yes   |
| Data areas and their retentivity                                   |   |
| Retentive data area (incl. timers, counters, flags), max.          | 512 kbyte; In total; available retentive memory for bit memories, timers, counters, DBs, and technology data (axes): 472 KB |
| Extended retentive data area (incl. timers, counters, flags), max. | 5 Mbyte; When using PS 6 0W 24/48/60 V DC HF  |
| Flag   |   |
| • Size, max.   | 16 kbyte  |
| Number of clock memories   | 8; 8 clock memory bit, grouped into one clock memory byte   |
| Data blocks  |   |
| Retentivity adjustable   | Yes   |
| Retentivity preset   | No  |
| Local data   |   |
| • per priority class, max.   | 64 kbyte; max. 16 KB per block  |
| Address area   |   |
| Number of IO modules   | 8 192; max. number of modules / submodules  |

| Article number   | 6ES7516-3AN02-0AB0  |
|--|---|
| I/O address area   | 0E37310-3AN02-0AD0  |
| • Inputs   | 32 kbyte; All inputs are in the process image   |
| Outputs  | 32 kbyte; All outputs are in the process image  |
| per integrated IO subsystem                              |   |
| – Inputs (volume)  | 8 kbyte   |
| <ul><li>Outputs (volume)</li></ul>                       | 8 kbyte   |
| per CM/CP  |   |
| <ul><li>Inputs (volume)</li></ul>                        | 8 kbyte   |
| <ul><li>Outputs (volume)</li></ul>                       | 8 kbyte   |
| Subprocess images  |   |
| • Number of subprocess images, max.                      | 32  |
| Hardware configuration  Number of distributed IO systems | 64; A distributed I/O system is characterized not only by the integration of distributed I/O via PROFINET or PROFIBUS communication modules, but also by the connection of I/O via AS-i master modules or links (e.g. IE/PB-Link) |
| Number of DP masters                                     |   |
| • integrated   | 1   |
| • Via CM   | 8; A maximum of 8 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total   |
| Number of IO Controllers                                 |   |
| • integrated   | 2   |
| • Via CM   | 8; A maximum of 8 CMs/CPs (PROFIBUS, PROFINET, Ethernet) can be inserted in total   |
| Rack   | 22 2011 24 11   |
| <ul> <li>Modules per rack, max.</li> </ul>               | 32; CPU + 31 modules  |
| Number of lines, max.                                    | 1   |
| PtP CM   |   |
| Number of PtP CMs  | the number of connectable PtP CMs is only limited by the number of available slots  |
| Time of day  |   |
| Clock  | Hardware clock  |
| • Type   |   |
| Backup time  | 6 wk; At 40 °C ambient temperature, typically   |
| Deviation per day, max.                                  | 10 s; Typ.: 2 s   |
| Operating hours counter  • Number                        | 16  |
|  |   |

| Article number  | CECTE16 2ANIO2 OADO  |
|---|--|
| Clock synchronization   | 6ES7516-3AN02-0AB0   |
| supported   | Yes  |
| to DP, master   | Yes  |
| • in AS, master   | Yes  |
| • in AS, slave  | Yes  |
| on Ethernet via NTP   | Yes  |
| Interfaces  |  |
| Number of PROFINET interfaces   | 2  |
| Number of PROFIBUS interfaces  1. Interface   | 1  |
| Interface types   |  |
| RJ 45 (Ethernet)  | Yes; X1  |
| Number of ports   | 2  |
| integrated switch   | Yes  |
| Protocols   |  |
| IP protocol   | Yes; IPv4  |
| PROFINET IO Controller  | Yes  |
| PROFINET IO Device  | Yes  |
| SIMATIC communication   | Yes  |
| Open IE communication   | Yes; Optionally also encrypted   |
| Web server  | Yes  |
| Media redundancy  | Yes  |
| PROFINET IO Controller  |  |
| Services  |  |
| <ul> <li>PG/OP communication</li> </ul>   | Yes  |
| <ul> <li>Isochronous mode</li> </ul>  | Yes  |
| <ul> <li>Direct data exchange</li> </ul>  | Yes; Requirement: IRT and isochronous mode (MRPD optional)   |
| – IRT   | Yes  |
| <ul><li>PROFlenergy</li></ul>   | Yes; per user program  |
| <ul> <li>Prioritized startup</li> </ul>   | Yes; Max. 32 PROFINET devices  |
| <ul> <li>Number of connectable IO Devices,<br/>max.</li> </ul>                                  | 256; In total, up to 1 000 distributed I/O devices can be connected via AS-i, PROFIBUS or PROFINET |
| <ul> <li>Of which IO devices with IRT, max.</li> </ul>  | 64   |
| <ul> <li>Number of connectable IO Devices for RT, max.</li> </ul>                               | 256  |
| <ul> <li>of which in line, max.</li> </ul>  | 256  |
| <ul> <li>Number of IO Devices that can be simultaneously activated/deactivated, max.</li> </ul> | 8; in total across all interfaces  |

| Article n                           | umber  | 6ES7516-3AN02-0AB0   |
|-------------------------------------|--|--|
|                                     | Number of IO Devices per tool, max.                | 8  |
| -                                   | Updating times                                     | The minimum value of the update time also depends on communication share set for PROFINET IO, on the number of IO devices, and on the quantity of configured user data |
| Update t                            | ime for IRT  |  |
| -                                   | for send cycle of 250 μs                           | 250 $\mu$ s to 4 ms; Note: In the case of IRT with isochronous mode, the minimum update time of 375 $\mu$ s of the isochronous OB is decisive                          |
| _                                   | for send cycle of 500 µs                           | 500 μs to 8 ms   |
| -                                   | for send cycle of 1 ms                             | 1 ms to 16 ms  |
| _                                   | for send cycle of 2 ms                             | 2 ms to 32 ms  |
| _                                   | for send cycle of 4 ms                             | 4 ms to 64 ms  |
| -                                   | With IRT and parameterization of "odd" send cycles | Update time = set "odd" send clock (any multiple of 125 $\mu$ s: 375 $\mu$ s, 625 $\mu$ s 3 875 $\mu$ s)   |
| -                                   | ime for RT   |  |
| _                                   | for send cycle of 250 µs                           | 250 μs to 128 ms   |
| _                                   | for send cycle of 500 µs                           | 500 μs to 256 ms   |
| _                                   | for send cycle of 1 ms                             | 1 ms to 512 ms   |
| _                                   | for send cycle of 2 ms                             | 2 ms to 512 ms   |
|                                     | for send cycle of 4 ms                             | 4 ms to 512 ms   |
|                                     | T IO Device  |  |
| Services                            | PG/OP communication                                | Yes  |
| _                                   |  | No   |
| _                                   | Isochronous mode                                   | Yes  |
| _                                   | IRT  |  |
| _                                   | PROFlenergy  | Yes; per user program  |
| _                                   | Shared device                                      | Yes  |
| _                                   | Number of IO Controllers with shared device, max.  | 4  |
| -                                   | activation/deactivation of I-devices               | Yes; per user program  |
|                                     | Asset management record                            | Yes; per user program  |
| 2. Interfa                          |  |  |
| Interface types                     |  | Yes; X2  |
| RJ 45 (Ethernet)                    |  |  |
| <ul> <li>Number of ports</li> </ul> |  | 1  |
| integrated switch                   |  | No   |

| Article number  | 6ES7516-3AN02-0AB0   |  |  |
|---|--|--|--|
| Protocols   |  |  |  |
| • IP protocol   | Yes; IPv4  |  |  |
| PROFINET IO Controller  | Yes  |  |  |
| PROFINET IO Device  | Yes  |  |  |
| SIMATIC communication   | Yes  |  |  |
| Open IE communication   | Yes; Optionally also encrypted   |  |  |
| Web server  | Yes  |  |  |
| Media redundancy  | No   |  |  |
| PROFINET IO Controller  |  |  |  |
| Services  |  |  |  |
| <ul> <li>PG/OP communication</li> </ul>   | Yes  |  |  |
| <ul> <li>Isochronous mode</li> </ul>  | No   |  |  |
| <ul> <li>Direct data exchange</li> </ul>  | No   |  |  |
| – IRT   | No   |  |  |
| <ul><li>PROFlenergy</li></ul>   | Yes; per user program  |  |  |
| <ul> <li>Prioritized startup</li> </ul>   | No   |  |  |
| <ul> <li>Number of connectable IO Devices,<br/>max.</li> </ul>                                  | 32; In total, up to 1 000 distributed I/O devices can be connected via AS-i, PROFIBUS or PROFINET  |  |  |
| <ul> <li>Number of connectable IO Devices for<br/>RT, max.</li> </ul>                           | 32   |  |  |
| <ul> <li>of which in line, max.</li> </ul>  | 32   |  |  |
| <ul> <li>Number of IO Devices that can be simultaneously activated/deactivated, max.</li> </ul> | 8; in total across all interfaces  |  |  |
| <ul> <li>Number of IO Devices per tool, max.</li> </ul>   | 8  |  |  |
| <ul> <li>Updating times</li> </ul>  | The minimum value of the update time also depends on communication share set for PROFINET IO, on the number of IO devices, and on the quantity of configured user data |  |  |
| Update time for RT  |  |  |  |
| <ul> <li>for send cycle of 1 ms</li> </ul>  | 1 ms to 512 ms   |  |  |

| Article number  | 6ES7516-3AN02-0AB0   |
|---|--|
| PROFINET IO Device  |  |
| Services  |  |
| <ul><li>PG/OP communication</li></ul>                                 | Yes  |
| <ul> <li>Isochronous mode</li> </ul>                                  | No   |
| – IRT   | No   |
| <ul><li>PROFlenergy</li></ul>   | Yes; per user program  |
| <ul> <li>Prioritized startup</li> </ul>                               | No   |
| <ul> <li>Shared device</li> </ul>                                     | Yes  |
| <ul> <li>Number of IO Controllers with shared device, max.</li> </ul> | 4  |
| <ul> <li>activation/deactivation of I-devices</li> </ul>              | Yes; per user program  |
| <ul> <li>Asset management record</li> </ul>                           | Yes; per user program  |
| 3. Interface  |  |
| Interface types   |  |
| • RS 485  | Yes; X3  |
| Number of ports   | 1  |
| Protocols   |  |
| PROFIBUS DP master  | Yes  |
| PROFIBUS DP slave   | No   |
| SIMATIC communication   | Yes  |
| PROFIBUS DP master  |  |
| <ul> <li>Number of connections, max.</li> </ul>                       | 48; for the integrated PROFIBUS DP interface   |
| Number of DP slaves, max.   | 125; In total, up to 1 000 distributed I/O devices can be connected via AS-i, PROFIBUS or PROFINET |
| Services  | W.   |
| <ul> <li>PG/OP communication</li> </ul>                               | Yes  |
| <ul><li>Equidistance</li></ul>  | Yes  |
| <ul><li>Isochronous mode</li></ul>                                    | Yes  |
| Activation/deactivation of DP slaves                                  | Yes  |
| Interface types   |  |
| RJ 45 (Ethernet)  | Yes  |
| • 100 Mbps  | Yes  |
| Autonegotiation   |  |
| <ul> <li>Autocrossing</li> </ul>                                      | Yes  |
| Industrial Ethernet status LED  | Yes  |
| RS 485  |  |
| • Transmission rate, max.   | 12 Mbit/s  |

| Article number  | 6ES7516-3AN02-0AB0  |
|---|---|
| Protocols   | 0E37310-3AN02-0AB0  |
| Number of connections   |   |
| Number of connections, max.   | 256; via integrated interfaces of the CPU and connected CPs / CMs   |
| <ul> <li>Number of connections reserved for<br/>ES/HMI/web</li> </ul>   | 10  |
| <ul> <li>Number of connections via integrated interfaces</li> </ul>     | 128   |
| <ul> <li>Number of S7 routing paths</li> </ul>                          | 16  |
| Redundancy mode   |   |
| H-Sync forwarding   | Yes   |
| Media redundancy  |   |
| <ul> <li>Media redundancy</li> </ul>                                    | only via 1st interface (X1)   |
| <ul><li>MRP</li><li>MRP interconnection, supported</li></ul>            | Yes; MRP Automanager according to IEC 62439-2<br>Edition 2.0, MRP Manager; MRP Client<br>Yes; as MRP ring node according to IEC 62439-2 |
| - Mike Interconnection, supported                                       | Edition 3.0   |
| <ul><li>MRPD</li></ul>  | Yes; Requirement: IRT   |
| <ul> <li>Switchover time on line break, typ.</li> </ul>                 | 200 ms; For MRP, bumpless for MRPD  |
| <ul> <li>Number of stations in the ring, max.</li> </ul>                | 50  |
| SIMATIC communication   |   |
| <ul> <li>PG/OP communication</li> </ul>                                 | Yes; encryption with TLS V1.3 pre-selected  |
| S7 routing  | Yes   |
| Data record routing   | Yes   |
| S7 communication, as server   | Yes   |
| S7 communication, as client   | Yes   |
| User data per job, max.   | See online help (S7 communication, user data size)  |
| Open IE communication   |   |
| • TCP/IP  | Yes   |
| <ul> <li>Data length, max.</li> </ul>                                   | 64 kbyte  |
| <ul> <li>several passive connections per port,<br/>supported</li> </ul> | Yes   |
| • ISO-on-TCP (RFC1006)  | Yes   |
| <ul> <li>Data length, max.</li> </ul>                                   | 64 kbyte  |
| • UDP   | Yes   |
| – Data length, max.   | 2 kbyte; 1 472 bytes for UDP broadcast  |
| <ul> <li>UDP multicast</li> </ul>                                       | Yes; Max. 5 multicast circuits  |
| • DHCP  | Yes   |
| • DNS   | Yes   |
|   |   |

| 4747744 241102 2422   |
|---|
| 6ES7516-3AN02-0AB0  |
| Yes   |
| Yes   |
| Yes   |
| Yes; Optional   |
|   |
| Yes; Standard and user pages  |
| Yes; Standard and user pages  |
|   |
| Yes; "Medium" license required  |
| Yes   |
| Yes   |
| Available security policies: None, Basic128Rsa15, Basic256Rsa15, Basic256Sha256 |
| "anonymous" or by user name & password  |
| 10  |
| 2 000   |
| 300   |
| 20  |
| 100   |
| 1   |
| 5   |
| 5 000   |
| 100   |
| 20  |
|   |

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|--|---|
| OPC UA Server  | Yes; Data access (read, write, subscribe), method call, custom address space          |
| <ul> <li>Application authentication</li> </ul>                                   | Yes   |
| <ul> <li>Security policies</li> </ul>  | Available security policies: None, Basic128Rsa15, Basic256Rsa15, Basic256Sha256       |
| <ul> <li>User authentication</li> </ul>  | "anonymous" or by user name & password  |
| <ul> <li>GDS support (certificate management)</li> </ul>                         | Yes   |
| <ul> <li>Number of sessions, max.</li> </ul>                                     | 48  |
| <ul> <li>Number of accessible variables, max.</li> </ul>                         | 100 000   |
| <ul> <li>Number of registerable nodes, max.</li> </ul>                           | 20 000  |
| <ul> <li>Number of subscriptions per session, max.</li> </ul>                    | 20  |
| <ul> <li>Sampling interval, min.</li> </ul>                                      | 100 ms  |
| <ul> <li>Publishing interval, min.</li> </ul>                                    | 200 ms  |
| <ul> <li>Number of server methods, max.</li> </ul>                               | 50  |
| <ul> <li>Number of inputs/outputs per server method, max.</li> </ul>             | 20  |
| <ul> <li>Number of monitored items, max.</li> </ul>                              | 2 000; for 1 s sampling interval and 1 s send interval                                |
| <ul> <li>Number of nodes for user-defined server interfaces, max.</li> </ul>     | 5 000   |
| Alarms and Conditions  | Yes   |
| <ul> <li>Number of program alarms</li> </ul>                                     | 200   |
| <ul> <li>Number of alarms for system diagnostics</li> </ul>                      | 100   |
| Further protocols  |   |
| • MODBUS   | Yes; MODBUS TCP   |
| Isochronous mode   |   |
| Equidistance   | Yes   |
| <b>S7 message functions</b> Number of login stations for message functions, max. | 64  |
| Program alarms   | Yes   |
| Number of configurable program messages, max.                                    | 10 000; Program messages are generated by the "Program_Alarm" block, ProDiag or GRAPH |
| Number of loadable program messages in RUN, max.                                 | 5 000   |
| Number of simultaneously active program alarms                                   |   |
| Number of program alarms   | 1 000   |
| Number of alarms for system diagnostics  | 200   |
| Number of alarms for motion technology<br>objects                                | 160   |

| Article number                                       | 6ES7516-3AN02-0AB0   |
|--|--|
| Test commissioning functions                         |  |
| Joint commission (Team Engineering)                  | Yes; Parallel online access possible for up to 8 engineering systems |
| Status block   | Yes; Up to 8 simultaneously (in total across all ES clients)         |
| Single step  | No   |
| Number of breakpoints                                | 8  |
| Status/control                                       |  |
| <ul> <li>Status/control variable</li> </ul>          | Yes  |
| • Variables  | Inputs/outputs, memory bits, DBs, distributed I/Os, timers, counters |
| <ul> <li>Number of variables, max.</li> </ul>        |  |
| <ul> <li>of which status variables, max.</li> </ul>  | 200; per job   |
| <ul> <li>of which control variables, max.</li> </ul> | 200; per job   |
| Forcing  |  |
| • Forcing  | Yes  |
| Forcing, variables                                   | Peripheral inputs/outputs  |
| Number of variables, max.                            | 200  |
| Diagnostic buffer                                    |  |
| • present  | Yes  |
| Number of entries, max.                              | 3 200  |
| <ul> <li>of which powerfail-proof</li> </ul>         | 500  |
| Traces   |  |
| Number of configurable Traces                        | 4; Up to 512 KB of data per trace are possible                       |
| Interrupts/diagnostics/status information            |  |
| Diagnostics indication LED                           |  |
| RUN/STOP LED   | Yes  |
| ERROR LED  | Yes  |
| MAINT LED  | Yes  |
| STOP ACTIVE LED                                      | Yes  |
| Connection display LINK TX/RX                        | Yes  |

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|--|---|
| Supported technology objects  Motion Control   | Yes; Note: The number of technology objects affects the cycle time of the PLC program; selection guide via the TIA Selection Tool |
| <ul> <li>Number of available Motion Control resources for technology objects</li> </ul>            | 2 400   |
| <ul> <li>Required Motion Control resources</li> </ul>  |   |
| <ul> <li>per speed-controlled axis</li> </ul>  | 40  |
| <ul> <li>per positioning axis</li> </ul>   | 80  |
| <ul> <li>per synchronous axis</li> </ul>   | 160   |
| <ul> <li>per external encoder</li> </ul>   | 80  |
| <ul> <li>per output cam</li> </ul>   | 20  |
| <ul><li>per cam track</li></ul>  | 160   |
| <ul><li>per probe</li></ul>  | 40  |
| Positioning axis   |   |
| <ul> <li>Number of positioning axes at motion<br/>control cycle of 4 ms (typical value)</li> </ul> | 7   |
| <ul> <li>Number of positioning axes at motion<br/>control cycle of 8 ms (typical value)</li> </ul> | 14  |
| Controller   |   |
| PID_Compact  | Yes; Universal PID controller with integrated optimization  |
| • PID_3Step  | Yes; PID controller with integrated optimization for valves   |
| • PID-Temp   | Yes; PID controller with integrated optimization for temperature  |
| Counting and measuring   |   |
| High-speed counter   | Yes   |
| Standards, approvals, certificates   |   |
| Suitable for safety functions  | No  |
| Ambient conditions  Ambient temperature during operation   |   |
| <ul> <li>horizontal installation, min.</li> </ul>  | -25 °C; No condensation   |
| • horizontal installation, max.  | 60 °C; Display: 50 °C, at an operating temperature of typically 50 °C, the display is switched off                                |
| • vertical installation, min.  | -25 °C; No condensation   |
| vertical installation, max.  | 40 °C; Display: 40 °C, at an operating temperature of typically 40 °C, the display is switched off                                |
| Ambient temperature during storage/transportation  |   |
| • min.   | -40 °C  |
| • max.   | 70 °C   |

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|---|--|
| Altitude during operation relating to sea level                   |  |
| Installation altitude above sea level, max.                       | 5 000 m; Restrictions for installation altitudes > 2 000 m, see manual |
| Configuration   |  |
| Programming   |  |
| Programming language  | V  |
| – LAD   | Yes  |
| - FBD   | Yes  |
| – STL   | Yes  |
| – SCL   | Yes  |
| – GRAPH   | Yes  |
| Know-how protection   |  |
| <ul> <li>User program protection/password protection</li> </ul>   | Yes  |
| Copy protection   | Yes  |
| Block protection  | Yes  |
| Access protection   |  |
| <ul> <li>protection of confidential configuration data</li> </ul> | Yes  |
| Password for display  | Yes  |
| Protection level: Write protection                                | Yes  |
| Protection level: Read/write protection                           | Yes  |
| Protection level: Complete protection                             | Yes  |
| Cycle time monitoring   |  |
| <ul> <li>lower limit</li> </ul>                                   | adjustable minimum cycle time  |
| • upper limit   | adjustable maximum cycle time  |
| Dimensions  |  |
| Width   | 70 mm  |
| Height  | 147 mm   |
| Depth   | 129 mm   |
| Weight approx   | QAE a  |
| Weight, approx.   | 845 g  |

# **General technical specifications**

You can find information on the general technical specifications, such as standards and approvals, electromagnetic compatibility, protection class, etc., in the S7-1500, ET 200MP System Manual (http://support.automation.siemens.com/WW/view/en/59191792).

# **Dimensional drawing**



This section contains the dimensional drawing of the module on the mounting rail, as well as a dimensional drawing with the front panel open. Always observe the specified dimensions for installation in cabinets, control rooms, etc.

### Dimensional drawings for CPU 1516-3 PN/DP

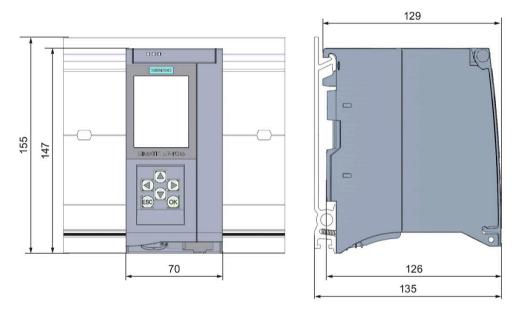


Figure A-1 Dimensional drawing of the CPU 1516-3 PN/DP

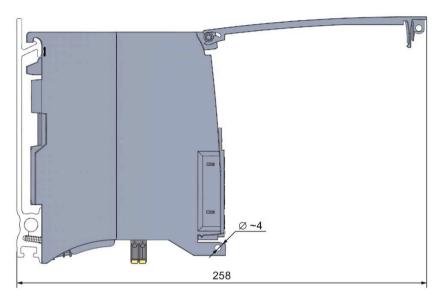


Figure A-2 CPU with open front panel